CHIEF PATRONS

**Prof. Shyam Lal Soni
(Director, NIT Uttarakhand)**

PATRONS

Dr. Gurinder Brar (Dean Acad)

Dr. Hariharan Muthusamy (Dean FW)

Dr. Dharmendra Tripathi (Dean R&C)

CONVENER

Dr. Tajinder Singh Arora

COORDINATORS

**Dr. Sarika Pal
Dr. Siva Kumar Tadepalli
Dr. Pankaj Kumar Pal
Dr. Tushar Goel**

INVITED EXPERTS

Senior scientists from SCL Chandigarh having more than 10 years of experience in Analog integrated circuits design

CONTACT PERSON

Dr. Tajinder Singh Arora
Assistant Professor and Head
Department of Electronics Engineering,
NIT Uttarakhand, Srinagar Garhwal, Uttarakhand
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Mobile: 9818671663

About NIT Uttarakhand

National Institute of Technology Uttarakhand is located in the hilly terrain of Srinagar, Pauri Garhwal, Uttarakhand. NIT Uttarakhand was established in 2009 under the Act of Parliament of India by the Ministry of Human Resource Development and designated with the status of "Institute of National Importance". Currently, NIT Uttarakhand is functional from two campuses i.e. Parent campus at Srinagar, Pauri Garhwal and Satellite campus at MNIT Jaipur.

Department of Electronics Engineering

Department of Electronics Engineering (EC) was established in the year 2010. The department offers Undergraduate (UG), Postgraduate (PG) and Ph.D. degree programs that provide students with the knowledge and tools they need to succeed in the Electronics and Communication Engineering. Research in the department focuses on various high-impact disciplines: Signal and Image Processing, Microwave antennas, Optical communication and VLSI design and technologies. Our faculty brings state-of-the-art research, development, and design experience into the classroom, ensuring that our students and alumni are able to apply for registration as professional engineers in all part of global engineering and the scientific community.

TEQIP-III Sponsored

One Week Workshop

On

Analog IC Design: Specifications to Chip

(February 17th - 21st, 2020)

Organised by

Department of Electronics Engineering

NIT Uttarakhand, Srinagar Garhwal,

Uttarakhand

Objectives of this Course

The objective of this workshop is to provide an exposure to the participants to the state-of-the-art in Analog IC Design and analysis. The participants will be given exposure to circuit to chip design process through EDA Tools.

Prerequisite: Participants to have already completed course on Circuit theory/network analysis, Transistor fundamentals.

Programme Topics This workshop will cover the following topics in details.

- A. Process-Design linkages and PDK
- B. MOS Models
- C. Controlled sources
- D. Diff Amp
- E. OPAMP and OTA
- F. Layout issues
- G. I/P pads
- H. Power pads and power grid
- I. Test and packaging issues
- J. Pre layout simulations
- K. Post layout simulations

Prospective Participants This STC is open for participants from Industries, PSUs and Academic Institutions Faculties, Ph.D. Research Scholars and Undergraduate (preferably final year only), Post-graduate students. The numbers of participants for the course are limited to thirty (30) on first come first serve basis.

Registration details

- ❖ A course registration fee of ₹ 1000/- (One thousand only) will be charged from participants and an online payment have to be made.
- ❖ There is **NO registration-fee** for participants from **TEQIP III institutions**.
- ❖ The fee covers the participation in the programme, registration material including tutorial notes, boarding (Tea/lunch) on all the days of the workshop. The travel and other expenses would have to be borne by the participants or their parent-organizations.
- ❖ No TA/DA will be paid to any participant.
- ❖ Accommodation on sharing basis may be provided to out-station participants only, upon prior request (on additional payment basis).
- ❖ Spot registration facility is also available.
- ❖ For online-payment, the bank account details are as follows:-

A/C Name: - National Institute of Technology Uttarakhand

Bank Name: - SBI, Srinagar-Garhwal
Account No: - 37530566069
IFSC Code: - SBIN0003181

Filled registration form along-with proof of online transaction must be e-mailed to: -
tsarora@nituk.ac.in

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1. Full Name…………………………………………....
2. Designation ………………………………………..
3. Organization……………………………………….
4. Academic Qualification……………………………
5. Age ……………………………………………………
6. Gender (Male/Female/Others)…….................
7. Mailing address ………………………….………....
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   ……………………………………………………………
8. Email ………………………………………………….
9. Phone/Mobile ……………………………………….

Place:

Signature of applicant